Fig. 1

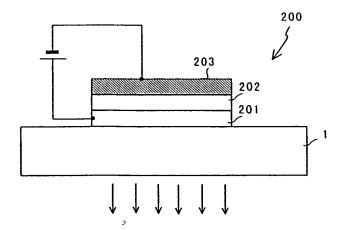


Fig. 2

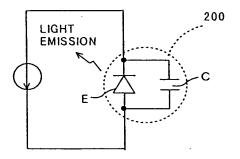


Fig. 3

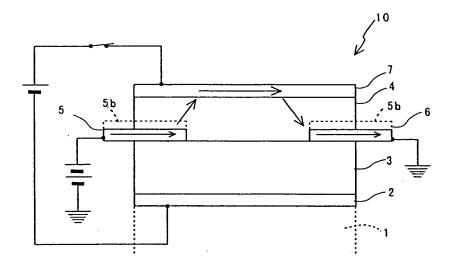


Fig. 4

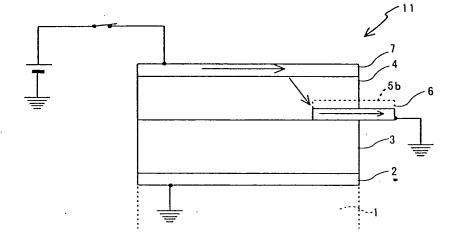


Fig. 5

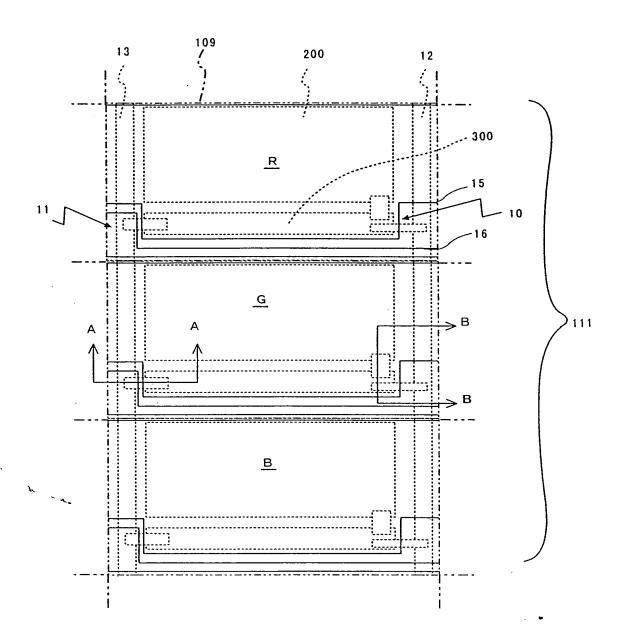
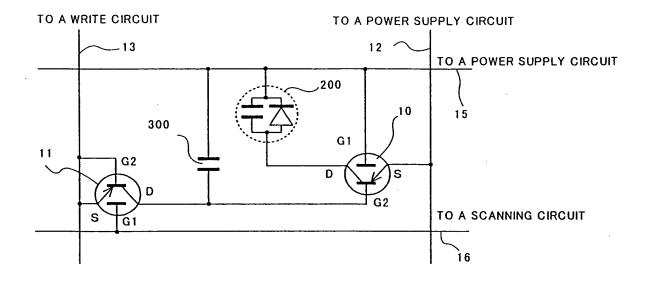


Fig. 6



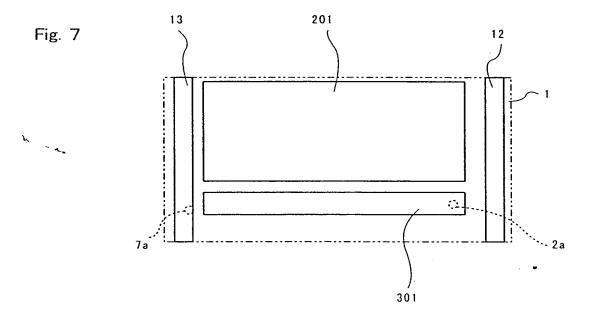


Fig. 8

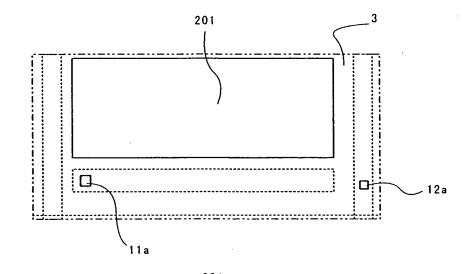


Fig. 9

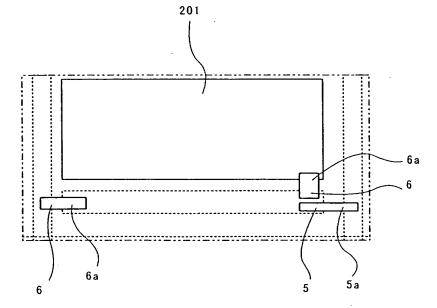


Fig. 10

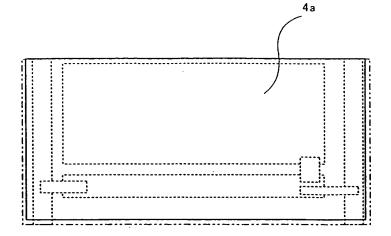


Fig. 11

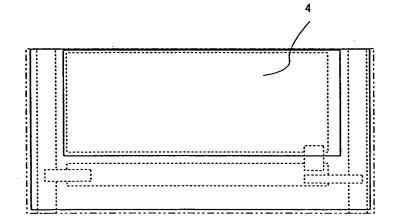
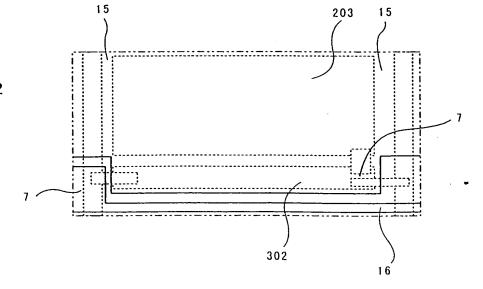
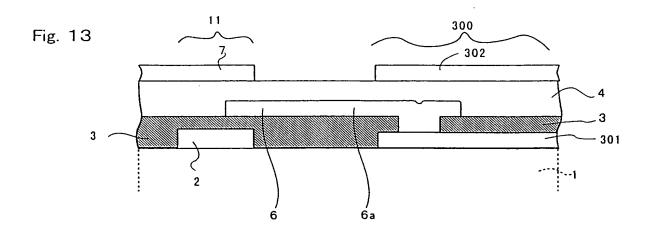
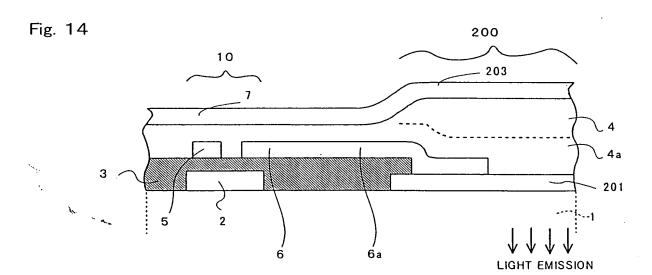
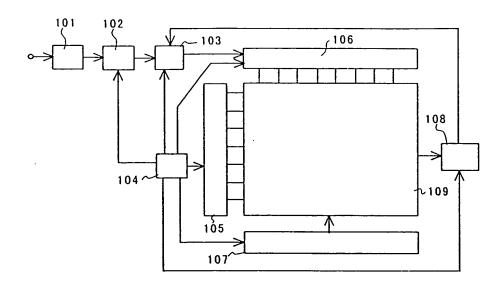


Fig. 12









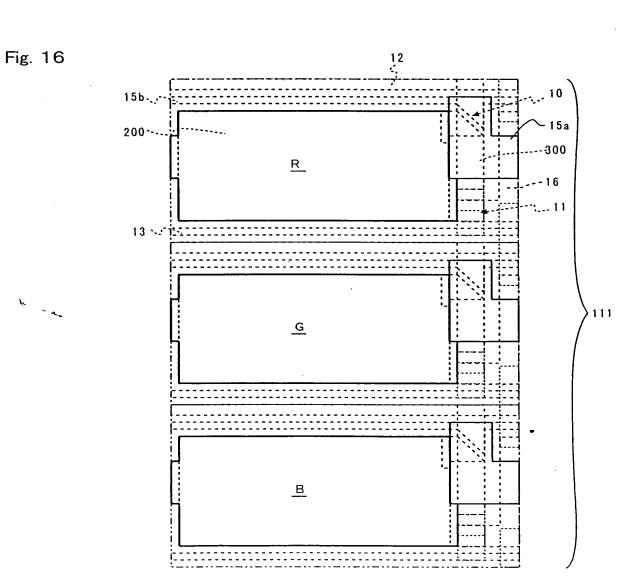


Fig. 17

TO A SCANNING CIRCUIT 12 TO A POWER SUPPLY CIRCUIT 15ь TO A POWER SUPPLY CIRCUIT G1 10-16 200 G2 300 300ь 11 13 s TO A WRITE CIRCUIT 15a

Fig. 18

12

66a

55a

302b

13

16b

13b

Fig. 19

-16a 201-

Fig. 20

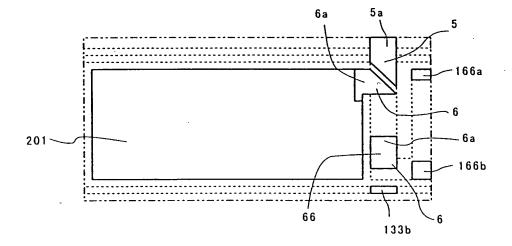
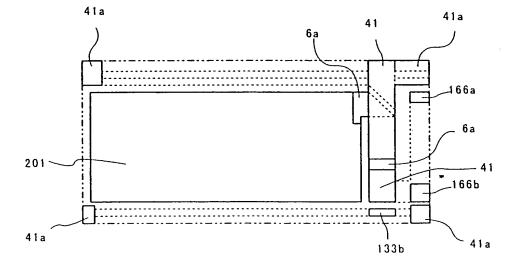


Fig. 2.1



301

16c

Fig. 22

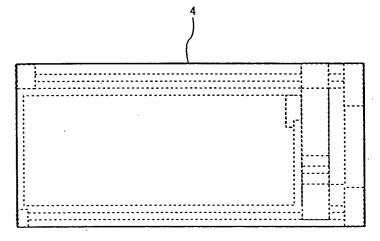


Fig. 24 203

Fig. 23

